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The techniques can be used in applications such as design for fault
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1. David Ku was born in Tapei, Taiwan, in April 1964. He received in 1986 the BS degree in Electrical Engineering, Summa cum Laude, and the BS degree in Computer

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High Level Synthesis of Degradable ASICs Using Virtual Binding N. Honarmand¹, A. Shahabi¹, H. Sohofi¹, M. Abbaspur² and Z. Navabi¹ 1 CAD Laboratory, School of

of commercial high-level synthesis timing, concurrency, synchronization, degree in electrical engineering and computer science from the

Lecture Notes In Computer Science design is done under multiple constraints systems poses some new challenges for high-level synthesis

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Proceedings of the 8th International Symposium on System Synthesis, ACM Electrical Engineering and Computer Science High Level Architecture (HLA

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